



Name: ESR4.2 Ahmet Cagri Bagbaba
IRP title: **EDA Tools and methodologies for high quality nanoelectronics systems**
From: CDNS
To: TUT
Period: 17-21. June, 2019

Activities during the secondment

- **Scope and objectives:**
1st year PhD attestation and discussion about dynamic slicing.
- **Activities:**
Presentation was done for 1st year PhD attestation. Next action points were discussed regarding dynamic slicing.
- **Main results achieved:**
The dynamic slicing paper “Accelerating Transient Fault Injection Campaigns by using Dynamic HDL Slicing” was published on NorCAS2019.
- **Next steps:**
 - Working on multi-point fault injection
 - Complete gate-level AutoSoC
- **Optional request for support or a technology/tool available at host:** No

Self-evaluation

Overall score: 5

Optional comments: None

Date of the report approval by the supervisor: 19.11.2019

