

**Name:** ESR4.1 Felipe Augusto da Silva  
**IRP title:** **EDA tools and methodologies for reliable nanoelectronic systems**  
**From:** CDN  
**To:** PDT  
**Period:** March 19 – March 22, 2019

### Activities during the secondment

- **Scope and objectives:**
  - Presentation of the AutoSoC benchmark
  - Discussions on collaborations with ESR2.3 Aneesh Balakrishnan (iROC)
- **Activities:**
  - Presentation of the AutoSoC benchmark initiative to the research group coordinated by Professor Matteo Reorda. The objective was to start collaborations using the benchmark.
  - Definition of collaborations with ESR2.3 on the identification of Untestable faults.
- **Main results achieved**
  - AutoSoC is currently being used by the research group at PDT.
  - Collaboration with ESR2.3 is still on going and already result in an IEEE publication: Josie E. Rodriguez Condia, Felipe A. Da Silva, S. Hamdioui, C. Sauer, M. Sonza Reorda, "Untestable faults identification in GPGPUs for safety-critical applications", 2019 IEEE International Conference on Electronics Circuits and Systems (ICECS), Genova, Italy (to appear).
- **Next steps**
  - Development of the ongoing collaborations
- **Optional request for support or a technology/tool available at host:**
  - NA

### Self-evaluation

**Overall score:** 5

*I consider this secondment successful, with regards to the research objectives achieved, skills developed, supervision quality, diversity of the resources. (Agree = 5 ... Disagree = 1)*

**Optional comments:** None

*Date of the report approval by the supervisor: December 04, 2019*

