

RESCUE 2017 - Workshop on Reliability, Security and Quality

ETS 2017 Fringe Workshop, May 25-26, 2017, Limassol, Cyprus, <http://rescue2017.eu/>

Early registration by **April 14** through the [ETS website](#).

Preliminary Program

Thursday, May 25

16:30 - 16:45 **Welcome**, *M. Jenihhin, F. Regazzoni, M. Sonza Reorda, O. Hadar*

16:45 - 17:45 Keynote **Counteracting benevolent and malicious fault in embedded systems**,
Israel Koren, Univ. Massachusetts, US

18:00 - 20:00 **Wine and Cheese Panel** jointly with the TESTA workshop

- (Tentative!) **Impact of standards to systems quality, reliability and security**
Experts from Robert Bosch, IROC, Cadence, and more. Moderated by Erik Larsson, Uni Lund, SW.

20:00 - 21:30 Fringe Workshops Reception with a light buffet dinner

Friday, May 26

8:45 - 10:00 Invited Talks

- **No Safety without Security**, *Peter Langendörfer, IHP, DE*
- **Impact of Dependability on EDA solutions for Safety Critical Industrial Applications**, *Vladimir Zivkovic, Cadence, DE*

10:00 - 11:00 Coffee / **Poster Session**

- An Overview of Cross-Layer Resilience Design Methods, *Mitko Veleski, Rolf Kraemer and Milos Krstic*
- On Fault Detection Efficiency of Reliability Checkers Obtained by Verification Assertion Qualification, *Ranganathan Hariharan, Behrad Niazmand and Jaan Raik*
- Design & development of public-key based authentication architecture for IoT devices using PUF, *Haji Akhundov and Said Hamdioui*
- Defeating Hardware Trojan through Software Obfuscation, *Andrea Marcelli, Marco Restifo, Ernesto Sanchez and Giovanni Squillero*
- *[More poster presentations to be announced soon]*

11:00 - 12:30 Session 1 **Structural approaches to test and security**

- Sense Amplifier Offset Voltage Mitigation Under Presence of BTI, *Daniel Kraak, Innocent Agbo, Mottaqiallah Taouil, Said Hamdioui, Pieter Weckx, Stefan Cosemans, Francky Catthoor and Wim Dehaene*
- Deterministic Online Path Delay Measurement Using Short Cycle Test Pattern, *Kentaro Kato, Umi Mori*
- Extended Hsiao-Code Error Correction Considering Double and Triple Bit Errors, *Heinrich T. Vierhaus and Petr Pfeifer*
- Towards Mixed Structural-Functional Models for Algebraic Fault Attacks on Ciphers, *Jan Burchard, Ange-Salome Messengekossano, Jan Horacek, Mael Gay, Bernd Becker, Tobias Schubert, Martin Kreuzer and Ilia Polian*

12:30 - 13:30 Lunch

13:30 - 15:00 Session 2 **Combining different levels of abstraction for validation, test and security**

- Modeling for multi-view interference analysis of design aspects in MPSoC designs, *Jüri Vain, Apneet Kaur, Leonidas Tsiopoulos, Jaan Raik and Maksim Jenihhin*
- Automatic improvement of SLT programs defect coverage by fault tolerance and debug solutions: summary of on-going works, *Riccardo Cantoro, Sara Carbonara, Eduardo Chielle, Fernanda Lima Kastensmidt, Michail Maniatakos, Ernesto Sanchez and Matteo Sonza Reorda*
- Defence from Covert Channel Cyber-attack over video stream payload, *Yoram Segal, Ofer Hadar, Raz Birman and Eldad Hadas*
- Quality Modeling of Approximated Polynomials Using Affine Arithmetic, *Farnaz Forooghifar, Carna Radojicic, Zainalabedin Navabi and Christoph Grimm*

15:00 - 15:30 Coffee

15:30 - 16:30 Invited Talks

- **The boundary between security and reliability**, *Miroslaw Malek, Univ. Lugano, CH*
- **SRAM - Physical Unclonable Functions: Removing the barriers of securing a broad range of IoT devices**, *Geert-Jan Schrijen (CTO), Intrinsic-ID, NL*

16:30 - 16:45 **Wrap-up**