

Test Spring School 2018

Langwedel, Germany
May 25-28, 2018

Held in conjunction with the
23rd IEEE European Test Symposium
www.ieee-ets.org

T.S.S@ETS
Test Spring School 18

Friday, May 25th

18:00-19:30 Welcome and Reception

Saturday, May 26th

08:00 **Functional Safety from Component to the Cloud** (part I and II)

R. Mariani, Intel Corporation

14:00 **In-Field Test for Functional Safety** (part I and II)

M. Sonza Reorda, Politecnico di Torino

Sunday, May 27th

08:00 **In-Field Test for Functional Safety** (part III)

M. Sonza Reorda, Politecnico di Torino

10:30 **Hardware Security and Cryptography Essentials** (part I, II and III)

I. Verbauwhede, KU Lueven

16:15 **Manufacturing Test of Secure Devices: Threats Introduced by Test Infrastructures** (part I)

G. Di Natale, LIRMM

Monday, May 28th

08:00 **Manufacturing Test of Secure Devices: Threats Introduced by Test Infrastructures** (part II and III)

G. Di Natale, LIRMM

14:00 **Track A: Automotive Test for Functional Safety**

Y. Zorian, Synopsis Armenia

Track B: Embedded System Security: Cryptographic Challenges and Physical Threats

T. Güneysu, Ruhr-Universität Bochum

Hardware Safety and Security

The European Test Symposium offers the 3 days Test Spring School (TSS@ETS 2018, May 25 - 28) for Ph.D. and M.Sc. students who will be introduced into modern safety and security technology. Renowned experts will give lectures and will cover the main challenges of safety and security of today's hardware infrastructures. The spring school also offers the state of the art design, test, verification and validation techniques tackling safety and security.

The overall theme of this year is Hardware Safety and Security. The first two lectures cover hardware functional safety including design, verification, validation and test. The third and fourth lectures cover hardware security, cryptographic hardware essentials and security threats of hardware infrastructures. One of the last two lectures covers in-system runtime test for functional safety whereas the other lecture covers authentication side channel attacks and intrusion in the context of hardware security. The spring school is organized in conjunction with ETS 2018, and the last two lectures are open for the general ETS attendees without additional fee. The spring school is also organized in cooperation with H2020 TWINN TUTORIAL project, H2020 RESCUE European Training Network and the project SHIVA (Secure Hardware in Information Processing) funded by the BW-Stiftung of the federal state of Baden-Württemberg, Germany.

TSS is offered to registered students at low cost rate. The spring school is also open for professionals at higher rates, however priority is given to students on first-come, first-served basis, as the number of attendees is strictly limited.

Test Spring School Venue

Etelsen Castle Hotel
Bremer Str. 2, 27299
Langwedel, Germany

www.schloss-etelsen.de

Organizing Committee

Görschwin Fey
Abraham Tibebu
Sebastian Huhn
Lisa Jungmann
Birthe Semken

Scientific Committee Chairs

Lorena Anghel
Hans-Joachim Wunderlich

Scientific Committee Members

Stephan Eggersglüß
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